

PATENT APPLICATION Attorney's Doc. No. 5038-263 Intel Doc. No. P12925

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Enclosed for filing is a patent application under 37 CFR 1.53(b) of:

Inventor: Jeffrey B. Davis

For: VOLTAGE MISMATCH TOLERANT INPUT/OUTPUT BUFFER

Enclos	sures:								
\boxtimes	Specification (pages 1-16); 28 claims (pages 17-20); abstract (page 21)								
$\overline{\boxtimes}$	EIGHT sheet(s) of FORMAL drawings (figures 1-8)								
	Combined Declaration and Power of Attorney								
	Newly executed (original or copy)	•							
\sqcap	Assignment with cover sheet								
_	Assignee Name and Address:	Intel Corporation (a Delaware corporation)							
	· ·	2200 Mission College Blvd.							
		P.O. Box 58119							
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CLAIMS AS FILED								
For	Number Filed	Number Extra	Rate		Basic Fee \$750			
Total Claims	28-20	8	x \$ 18	=	\$144.00			
Independent Claims	5-3	2	x \$ 84	=	\$168.00			
TOTAL FILING FEE					\$1062.00			



 \boxtimes PTO Form 2038 authorizing credit card payment in the amount of \$1062 for the above-listed filing fee is enclosed. \boxtimes

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Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C. ATTORNEYS FOR INTEL CORPORATION

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